

1 APPLICATION DATA SHEET (37 C.F.R. 1.76)

3 INVENTOR & RESIDENCE: Shekar Mallikarjunaswamy, San Jose, CA

4 CORRESPONDENCE ADDRESS: Eugene H. Valet
5 Valet.Patents@verizon.net
6 314 10th Ave. South
7 Edmonds WA 98020-3312
8 425-672-3147 fax 640-0525

9 TITLE: ESD Protection for Integrated Circuits

10 DRAWING SHEETS: 9 (informal)

11 SUGGESTED PUBLICATION
12 FIGURE NO.: FIG. 3

13 DOCKET NO.: M081

14 TYPE: UTILITY

15 ATTORNEY, REG. NO.: J. Vincent Tortolano, Reg. No. 31,433
16 Eugene H. Valet, 31,435

17 PRIORITY INFORMATION: n/a

18 ASSIGNEE: MICREL, Incorporated, San Jose, CA